

# ASM1233D-L/D/M

rev 1.3

### Low Power, 5V/3.3V, µP Reset, Active LOW, Open-Drain Output

#### **General Description**

The ASM1233D-L/1233D/1233M are voltage supervisors with low-power, 5/3.3V  $\mu$ P Reset, with an active LOW, open-drain output. Maximum supply current over temperature is 15 $\mu$ A for 3.3V devices and 20 $\mu$ A for 5V devices.

The ASM1233D-L/1233D/1233M generates an active LOW reset signal whenever the monitored supply is out of tolerance. A precision reference and comparator circuit monitors power supply ( $V_{CC}$ ) level. The tolerance are 5%,10% and 15%. When an out-of-tolerance condition is detected, an internal power-fail signal is generated which forces an active LOW reset signal. After  $V_{CC}$  returns to an in-tolerance condition, the reset signal remains active for 350ms to allow the power supply and system microprocessor to stabilize.

The ASM1233D-L/1233D/1233M is designed with an opendrain output stage and operates over the extended industrial temperature range. These devices are available in compact SOT-223, SO-8 and TO-92 packages.

Other low power products in this family include ASM1810/11/ 12/15/16/17.

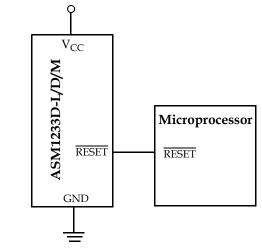
### **Key Features**

- Low Supply Current
  - 15µA maximum (<=3.6V), 20µA maximum (5.5V)
- Automatically restarts a microprocessor after power failure
- 350ms reset delay after V<sub>CC</sub> returns to an in-tolerance condition
- Active LOW power-up reset, 5kΩ internal pull-up
- Precision temperature-compensated voltage reference and comparator
- Eliminates external components
- Low-cost SOT-223/SO-8/TO-92 packages
- Operating temperature: -40°C to +85°C

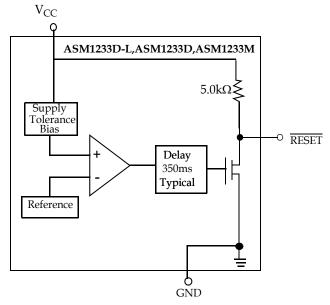
### Applications

- Set-top boxes
- Cellular phones
- PDAs
- Energy management systems
- Embedded control systems
- Printers
- Single board computers

### **Typical Operating Circuit**



#### **Block Diagram**



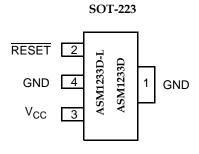
Alliance Semiconductor 2575 Augustine Drive . Santa Clara, CA 95054 . Tel: 408.855.4900 . Fax: 408.855.4999 . www.alsc.com

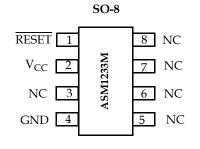
#### Notice: The information in this document is subject to change without notice

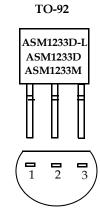


# ASM1233D-L/D/M

## rev 1.3 Pin Configuration







## **Pin Description**

	Pi					
TO-92 ASM1233D-L ASM1233D	TO-92 ASM1233M	SO-8	SOT-223	Pin Name	Description	
1	3	4	1,4	GND	Ground.	
2	1	1	2	RESET	Active LOW reset output.	
3	2	2	3	V <sub>CC</sub>	Power supply input.	
-	-	3,5,6,7&8	-	NC	No connection	



# ASM1233D-L/D/M

#### rev 1.3

#### **Application Information**

#### **Operation - Power Monitor**

The ASM1233D-L/1233D/1233M detects out-of-tolerance power supply conditions. It resets a processor during powerup, power-down and generates a reset to the system processor when the monitored power supply voltage is below the reset threshold. When an out-of-tolerance  $V_{CC}$  voltage is detected, the RESET signal is asserted. On power-up, RESET is kept active (LOW) for approximately 350ms after the power supply voltage has reached the selected tolerance. This allows the power supply and microprocessor to stabilize before RESET is released.

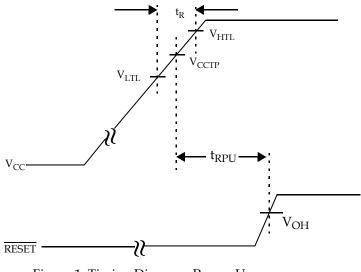


Figure 1: Timing Diagram: Power-Up

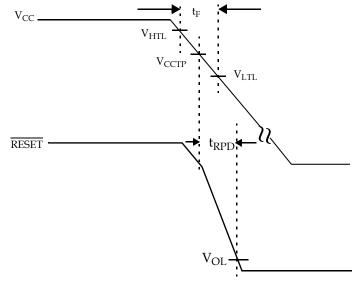


Figure 2: Timing Diagram: Power-Down



#### rev 1.3

### **Absolute Maximum Ratings**

Parameter	Min	Max	Unit		
Voltage on V <sub>CC</sub>	-0.5	7	V		
Voltage on RESET	-0.5	V <sub>CC</sub> + 0.5	V		
Operating Temperature Range	-40	85	°C		
Soldering Temperature (for 10 sec)		260	°C		
Storage Temperature	-55	125	°C		
ESD rating HBM MM		2 200	KV V		
NOTE: These are stress ratings only and functional use is not implied. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.					



#### rev 1.3

#### **Electrical Characteristics**

Unless otherwise noted,  $V_{CC} = 5V \pm 10\%$  and specifications are over the operating temperature range of -40°C to +85°C. All voltages are referenced to ground.

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
Supply Voltage	V <sub>CC</sub>		1.2		5.5	V	
Output Malta as	V <sub>OL</sub>	RESET asserted	-	-	0.4		
Output Voltage	V <sub>OH</sub>	Ι <sub>ΟUT</sub> < 500 μΑ	V <sub>CC</sub> -0.5V	V <sub>CC</sub> -0.1V		V	
Output Current	I <sub>OL</sub>	Output = 0.4V	8			mA	
Operating Current	loo	$V_{CC}$ < 5.5V, RESET output open		8	20	μA	
Operating Ourient	100	$V_{CC} < =3.6V, \overline{RESET}$ output open	1.2   -   V <sub>CC</sub> -0.5V   8   2.98   2.8   2.64   4.5   4.25   4.0   4.25   4.0   4.25   3.5   3.5   200   250   300   0	6	15	μΛ	
		ASM1233D-LZ-5	2.98	3.06	3.15		
		ASM1233D-LZ-10	2.8	2.88	2.97		
		ASM1233D-LZ-15	2.64	2.72	2.8		
		ASM1233DZ-5	4.5	4.625	4.74		
V <sub>CC</sub> Trip Point	V <sub>CCTP</sub>	ASM1233DZ-10	4.25	4.375	4.49	V	
		ASM1233DZ-15	4.0	4.125	4.24		
		ASM1233M-5	4.25	4.375	4.49		
		ASM1233M-55	4.5	4.625	4.75		
		ASM1233M-3	2.64	2.72	2.8	-	
		ASM1233D, ASM1233MS-5, ASM1233MS-55			4.75		
Voltage High Trip Level	V <sub>HTL</sub>	ASM1233MS-3			3.14	V	
		$\begin{array}{c c c c c c c } \hline V_{OL} & $\overline{\texttt{RESET}}$ asserted & - & - & - & \\ \hline V_{OH} & $I_{OUT} < 500 \ \mu\text{A} & $V_{CC} - 0.5V & $V_{CC} - 0.1V \\ \hline I_{OL} & $Output = 0.4V & $8 \\ \hline \\ \hline & $V_{CC} < = 3.6V, $\overline{\texttt{RESET}}$ output open & $8$ \\ \hline & $V_{CC} < = 3.6V, $\overline{\texttt{RESET}}$ output open & $6$ \\ \hline & $ASM1233D - LZ - 5 & $2.98 & $3.06$ \\ \hline & $ASM1233D - LZ - 10 & $2.8 & $2.88$ \\ \hline & $ASM1233D - LZ - 15 & $2.64 & $2.72$ \\ \hline & $ASM1233D - LZ - 15 & $2.64 & $2.72$ \\ \hline & $ASM1233D - LZ - 15 & $4.5 & $4.625$ \\ \hline & $ASM1233D - LZ - 15 & $4.0 & $4.125$ \\ \hline & $ASM1233D - LZ - 15 & $4.0 & $4.125$ \\ \hline & $ASM1233D - 10 & $4.25$ & $4.375$ \\ \hline & $ASM1233D - 15 & $4.0 & $4.125$ \\ \hline & $ASM1233M - 55 & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55 & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.625$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233M - 55$ & $4.5$ & $4.5$ \\ \hline & $ASM1233D - L$ & $ASM1233M & $200$ & $350$ \\ \hline & $t_{RPD}$ & $ASM1233D - L$ & $ASM1233M & $200$ & $350$ \\ \hline & $t_{RPD}$ & $ASM1233D - L$ & $ASM1233M & $200$ & $350$ \\ \hline & $t_{RPD}$ & $ASM1233D - L$ & $ASM1233M & $200$ & $350$ \\ \hline & $t_{RP}$ & $ASM1233D - L$ & $ASM1233M & $4.5$ \\ \hline & $ASM1233$	3.06				
					4.00		
Voltage Low Trip Level	V <sub>LTL</sub>	ASM1233MS-3			2.48	V	
		ASM1233D-L			2.3		
Internal Pull-up Resistor	R <sub>P</sub>		3.5	5.0	7.5	kΩ	
Output Capacitance	C <sub>OUT</sub>				10	pF	
V <sub>CC</sub> Detect to RESET Low	t <sub>RPD</sub>			2	10	μs	
V <sub>CC</sub> Detect to RESET High	+	ASM1233D-L, ASM1233M	200	350	500		
VCC Delect to RESET High	<sup>i</sup> RPU	ASM1233D	250	350	450	ms	
V <sub>CC</sub> Slew Rate (V <sub>HTL</sub> - V <sub>LTL</sub> )	t <sub>F</sub>		300			μs	
V <sub>CC</sub> Slew Rate (V <sub>LTL</sub> - V <sub>HTL</sub> )	t <sub>R</sub>		0			ns	
Note: A $1k\Omega$ resistor maybe requ	uired in some a	applications for proper operation of the	e microproces	sor reset contr	ol circuit		



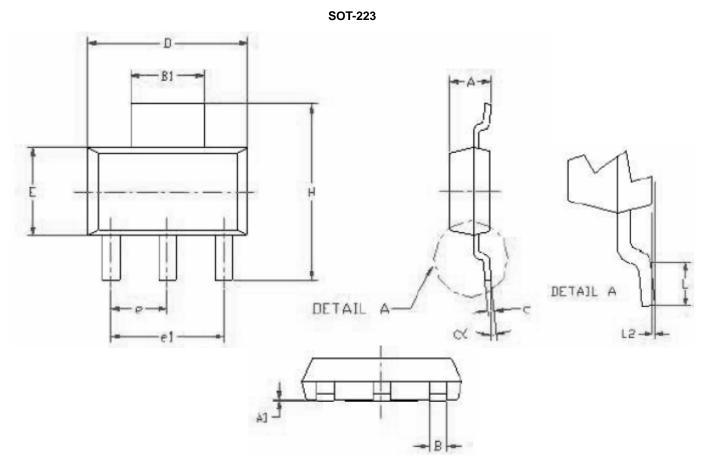
## rev 1.3 Family Selection Guide

Part #	RESET Voltage (V)	RESET Time (ms)	Output Stage	RESET Polarity
ASM1810	4.620, 4.370, 4.120	150	Push-Pull	LOW
ASM1811	4.620, 4.350, 4.130	150	Open-Drain	LOW
ASM1812	4.620, 4.350, 4.130	150	Push-Pull	HIGH
ASM1815	3.060, 2.880, 2.550	150	Push-Pull	LOW
ASM1816	3.060, 2.880, 2.550	150	Open-Drain	LOW
ASM1817	3.060, 2.880, 2.550	150	Push-Pull	HIGH
ASM1233D	4.625, 4.375, 4.125	350	Open-Drain	LOW
ASM1233M	4.625, 4.375, 2.720	350	Open-Drain	LOW
ASM1233D-L	3.06, 2.880, 2.720	350	Open-Drain	LOW



### rev 1.3

Package Information

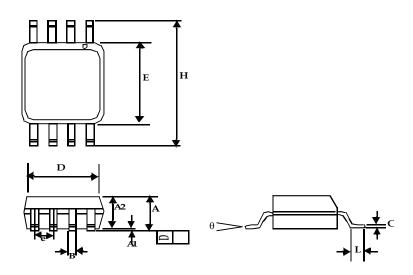


Symbol	Dimension	s in Inches	Dimensions in millimeters		
Symbol	Min Max Min		Min	Max	
A	0.061	0.071	1.55	1.80	
A1	0.0008	0.004	0.02	0.10	
В	0.024	0.031	0.60	0.80	
B1	0.114	0.122	2.90	3.10	
С	0.009	0.013	0.24	0.32	
D	0.248	0.264	6.30	6.70	
E	0.130	0.146	3.30	3.70	
е	0.090	BSC	2.30	BSC	
e1	0.181 BSC		4.60	BSC	
Н	0.264	0.287	6.70	7.30	
L	0.036 MIN		0.91 MIN		
L2	0.002	4 MIN	0.06 BSC		
α	0°	6°	0°	6°	



rev 1.3

Plastic SO-8 (8-Pin)



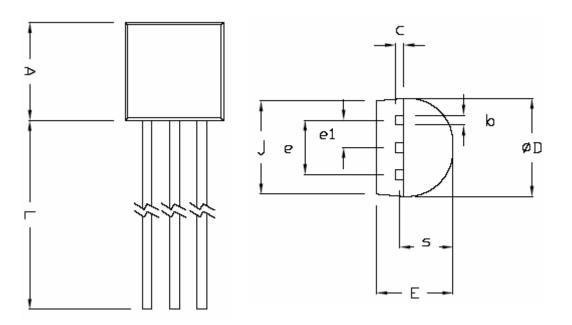
	Dimension	s in Inches	Dimensions in Millimeters		
	Min	Max	Min	Max	
Plastic SO-8 (8-Pin)					
А	0.053	0.069	1.35	1.75	
A1	0.004	0.010	0.10	0.25	
A2	0.049	0.059	1.25	1.50	
В	0.012	0.020	0.31	0.51	
С	0.007	0.010	0.18	0.25	
D	0.193	BSC	4.90 BSC		
Е	0.154 BSC		3.91 BSC		
е	0.050 BSC		1.27 BSC		
Н	0.236	BSC	6.00	BSC	
L	0.016	0.050	0.41	1.27	
θ	0°	8°	0°	8°	



## ASM1233D-L/D/M

rev 1.3

To-92 (3-Pin)



	Dimension	s in Inches	Dimensions in Millimeters		
	Min	Мах	Min	Мах	
		TO-92			
А	0.175	0.185	4.445	4.699	
b	0.016	0.020	0.406	0.508	
С	0.014	0.016	0.356	0.406	
φD	0.175	0.185	4.445	4.699	
E	0.138	0.144	3.505	3.658	
е	0.098	0.102	2.489	2.591	
e1	0.045	0.055	1.143	1.397	
j	0.168	0.174	4.269	4.420	
L	0.500	0.585	12.7	14.86	
S	0.095	0.099	2.413	2.515	



### rev 1.3

## **Ordering Information**

Part Number**	RESET Output Voltage	RESET Tolerance	RESET Time	Open Drain Output Stage*	RESET Polarity	Package	Package Marking
TIN - LEAD DEVICES							
ASM1233D-L-5	3.06	5%	350 ms	•	LOW	3L TO-92	ASM1233D-L-5
ASM1233D-L-10	2.88	10%	350 ms	•	LOW	3L TO-92	ASM1233D-L-10
ASM1233D-L-15	2.72	15%	350 ms	•	LOW	3L TO-92	ASM1233D-L-15
ASM1233D-LZ-5	3.06	5%	350 ms	•	LOW	4L SOT-223	RVLL
ASM1233D-LZ-10	2.88	10%	350 ms	•	LOW	4L SOT-223	RWLL
ASM1233D-LZ-15	2.72	15%	350 ms	•	LOW	4L SOT-223	RXLL
ASM1233D-5	4.625	5%	350 ms	•	LOW	3L TO-92	ASM1233D-5
ASM1233D-10	4.375	10%	350 ms	•	LOW	3L TO-92	ASM1233D-10
ASM1233D-15	4.125	15%	350 ms	•	LOW	3L TO-92	ASM1233D-15
ASM1233DZ-5	4.625	5%	350 ms	•	LOW	4L SOT-223	RSLL
ASM1233DZ-10	4.375	10%	350 ms	•	LOW	4L SOT-223	RTLL
ASM1233DZ-15	4.125	15%	350 ms	•	LOW	4L SOT-223	RULL
ASM1233M-55	4.625	5%	350 ms	•	LOW	3L TO-92	ASM1233M-55
ASM1233M-5	4.375	10%	350 ms	<b>♦</b>	LOW	3L TO-92	ASM1233M-5
ASM1233M-3	2.72	15%	350 ms	•	LOW	3L TO-92	ASM1233M-3
ASM1233MS-55	4.625	5%	350 ms	•	LOW	8L SOIC	ASM1233MS-55
ASM1233MS-5	4.38	10%	350 ms	•	LOW	8L SOIC	ASM1233MS-5
ASM1233MS-3	2.72	15%	350 ms	<b>♦</b>	LOW	8L SOIC	ASM1233MS-3
LEAD FREE DEVICE	S						
ASM1233D-L-5F	3.06	5%	350 ms	•	LOW	3L TO-92	ASM1233D-L-5F
ASM1233D-L-10F	2.88	10%	350 ms	•	LOW	3L TO-92	ASM1233D-L-10F
ASM1233D-L-15F	2.72	15%	350 ms	•	LOW	3L TO-92	ASM1233D-L-15F
ASM1233D-LZ-5F	3.06	5%	350 ms	•	LOW	4L SOT-223	KVLL
ASM1233D-LZ-10F	2.88	10%	350 ms	•	LOW	4L SOT-223	KWLL
ASM1233D-LZ-15F	2.72	15%	350 ms	•	LOW	4L SOT-223	KXLL
ASM1233D-5F	4.625	5%	350 ms	•	LOW	3L TO-92	ASM1233D-5F
ASM1233D-10F	4.375	10%	350 ms	•	LOW	3L TO-92	ASM1233D-10F
ASM1233D-15F	4.125	15%	350 ms	•	LOW	3L TO-92	ASM1233D-15F
ASM1233DZ-5F	4.625	5%	350 ms	•	LOW	4L SOT-223	KSLL
ASM1233DZ-10F	4.375	10%	350 ms	•	LOW	4L SOT-223	KTLL
ASM1233DZ-15F	4.125	15%	350 ms	•	LOW	4L SOT-223	KULL
ASM1233M-5F	4.375	5%	350 ms	•	LOW	3L TO-92	ASM1233M-5F
ASM1233M-55F	4.625	10%	350 ms	<b>♦</b>	LOW	3L TO-92	ASM1233M-55F
ASM1233M-3F	2.72	15%	350 ms	•	LOW	3L TO-92	ASM1233M-3F
ASM1233MS-5F	4.38	5%	350 ms	•	LOW	8L SOIC	ASM1233MS-5F
ASM1233MS-55F	4.625	10%	350 ms	•	LOW	8L SOIC	ASM1233MS-55F
ASM1233MS-3F	2.72	15%	350 ms	•	LOW	8L SOIC	ASM1233MS-3F





Alliance Semiconductor Corporation 2575, Augustine Drive, Santa Clara, CA 95054 Tel: 408 - 855 - 4900 Fax: 408 - 855 - 4999 www.alsc.com Copyright © Alliance Semiconductor All Rights Reserved Part Number: ASM1233D-L/D/M Document Version: 1.3

© Copyright 2003 Alliance Semiconductor Corporation. All rights reserved. Our three-point logo, our name and Intelliwatt are trademarks or registered trademarks of Alliance. All other brand and product names may be the trademarks of their respective companies. Alliance reserves the right to make changes to this document and its products at any time without notice. Alliance assumes no responsibility for any errors that may appear in this document. The data contained herein represents Alliance's best data and/or estimates at the time of issuance. Alliance reserves the right to change or correct this data at any time, without notice. If the product described herein is under development, significant changes to these specifications are possible. The information in this product data sheet is intended to be general descriptive information for potential customers and users, and is not intended to operate as, or provide, any guarantee or warrantee to any user or customer. Alliance does not assume any responsibility or liability arising out of the application or use of any product described herein, and disclaims any express or implied warranties related to the sale and/or use of Alliance products including liability or warranties related to fitness for a particular purpose, merchantability, or infringement of any intellectual property rights, except as express agreed to in Alliance's Terms and Conditions of Sale. The purchase of products from Alliance). All sales of Alliance products are made exclusively according to Alliance's Terms and Conditions of Sale. The purchase of products from Alliance does not assume or failance or third parties. Alliance does not authorize its products for use as critical components in life-supporting systems where a malfunction or failuance or third parties. Alliance does not authorize its products for use as critical components in life-supporting systems where a malfunction or failure may reasonably be expected to result in significant injury to the user, and the inclusion of Alliance produ